

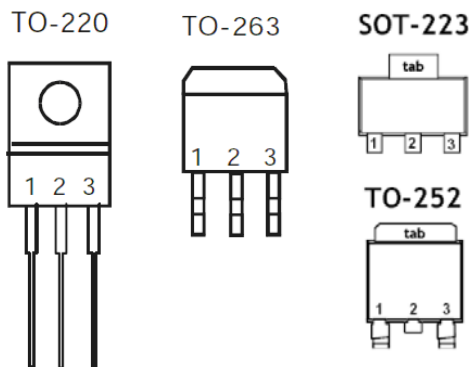
Features

- Adjustable or fixed output
- Output current of 1A
- Low dropout - 1.3V typ. at 1A output current
- 0.04% line regulation
- 0.2 % load regulation
- 100% thermal limit burn-in
- Fast transient response

Applications

- High efficiency linear regulators
- Post regulators for switching supplies
- Adjustable power supply

Package Pin Out



Pin	Function
1	ADJ/GND
2/ tab	OUT
3	IN

General Description

The LD6351 series of positive adjustable and fixed regulators is designed to provide 1A with high efficiency. All the internal circuitry is designed to operate down to 1.4V input-to-output differential.

On-chip trimming adjusts the reference voltage to 1%. The typical current limit value of 1.5A allows the stress on both the regulator and the power source circuitry to be minimized under overload conditions.

LD6351 can operate using MLCCs in the capacitance range of 2μF to 10μF.

Ordering Information

Part No.	Package	Packing Options	
		Tube (TU)	Tape & Reel (TR)
LD6351	SOT223	–	LD6351L8-TRX
	TO220	LD6351T3-TUX	LD6351T3-TRX
	TO252	LD6351T6-TUX	LD6351T6-TRX
	TO263	LD6351T8-TUX	LD6351T8-TRX

Voltage Selection

Part Number	Output Voltage
LD6351L8-TRX	Adjustable
LD6351L8-TRA	1.2V
LD6351L8-TRB	1.5V
LD6351L8-TRC	1.8V
LD6351L8-TRD	2.5V
LD6351L8-TRE	2.85V
LD6351L8-TRF	3.0V
LD6351L8-TRG	3.3V
LD6351L8-TRH	3.5V
LD6351L8-TRI	5.0V

- Package material default is “Green” package.

Product Marking

LD8888	◇ Line 1 – “LD” is a fixed character
SSSSS...	8888: product name
●	◇ Line 2 – SSSSS...: lot number

Absolute Maximum Ratings

Parameter	Maximum	Unit
Power dissipation	Internally limited	W
Input voltage	20	V
Operating junction temperature range	-40 to 125	°C
Storage temperature	-65 to 150	°C
Lead temperature (soldering, 10 sec)	300	°C

The values beyond the boundaries of absolute maximum rating may cause the damage to the device. Functional operation in this context is not implied. Continuous use of the device at the absolute rating level might influence device reliability. All voltages have their reference to device ground.

Electrical Characteristics

$I_{LOAD} = 0\text{mA}$ and $T_J = 25^\circ\text{C}$ unless specified, otherwise minimum and maximum values are guaranteed by production testing requirements.

Parameter	Symbol	Condition	Min	Typ.	Max	Unit
Reference voltage ^{*2}	V_{REF}	$V_{IN} = 5\text{V}, I_{LOAD} = 10\text{mA}$ ^{*6}	1.232	1.250	1.268	V
		$1.5\text{V} \leq V_{IN} - V_{OUT} \leq 10\text{V}, I_{LOAD} = 10\text{mA to } 1\text{A}$, ^{*1*6}	1.225	1.250	1.275	V
Accuracy of output voltage ^{*2}	$A_{CCVOUTF}$	$V_{IN} = V_{OUT} + 1.5\text{V}$, Varied from nominal V_{OUT} ^{*5}	-1.5	–	+1.5	%
		$1.5\text{V} \leq V_{IN} - V_{OUT} \leq 10\text{V}, I_{LOAD} = 0\text{mA to } 1\text{A}$, ^{*1*5}	-2	–	+2	%
		$V_{OUT} = 1.2\text{V}$, ^{*1}	-3	–	+2	%
Accuracy of output voltage at wafer testing	$A_{CCVOUTW}$	$V_{IN} = V_{OUT} + 1.5\text{V}, I_{LOAD} = 10\text{mA}$	-0.6	0	+0.6	%
Line regulation	R_{LINE}	$I_{LOAD} = 10\text{mA}, 1.5\text{V} \leq V_{IN} - V_{OUT} \leq 10\text{V}$, ^{*1}	–	0.04	0.20	%
Load regulation ^{*2}	R_{LOAD}	$V_{IN} = V_{OUT} + 1.5\text{V}, I_{LOAD} = 10\text{mA to } 1\text{A}$, ^{*1}	–	0.2	0.40	%
Minimum load current	I_{LOAD}	$V_{IN} = 5\text{V}, V_{ADJ} = 0\text{V}$, ^{*1*6}	–	2	7	mA
GND pin current	I_{GND}	$V_{IN} = V_{OUT} + 1.5\text{V}, I_{LOAD} = 10\text{mA to } 1\text{A}$, ^{*1*5}	–	3.5	10	mA
ADJ pin current	I_{ADJ}	$1.5\text{V} \leq V_{IN} - V_{OUT} \leq 10\text{V}, I_{LOAD} = 10\text{mA}$, ^{*1*6}	–	35	60	μA
Current limit	I_{LIMIT}	$V_{IN} - V_{OUT} = 1.5\text{V}$, ^{*1}	1	1.5	2	A
Ripple rejection ^{*3}	R_{RIPPLE}	$V_{IN} - V_{OUT} = 2.5\text{V}, I_{LOAD} = 1\text{A}$	60	–	–	dB
Dropout voltage ^{*2*4}	V_{DROPUT}	$I_{LOAD} = 1\text{A}$, ^{*1}	–	1.20	1.40	V
Temperature coefficient	T_{COEF}	$V_{IN} - V_{OUT} = 1.5\text{V}, I_{LOAD} = 10\text{mA}$, ^{*1}	–	–	0.015	%/°C

1. Denotes “apply over the full temperature range” – $40^\circ\text{C} \leq T_J \leq 125^\circ\text{C}$

2. Low duty pulse testing with Kelvin connections is required

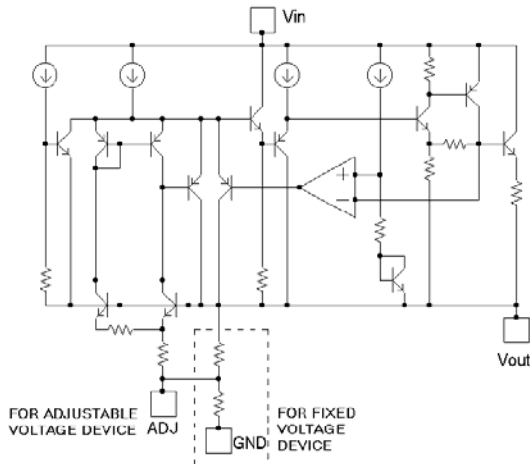
3. 120Hz input ripple ($C_{ADJ} = 25\mu\text{F}$ for the ADJ version)

4. $V_{OUT}, V_{REF} = 1\%$

5. V_{OUT} output fixed version only

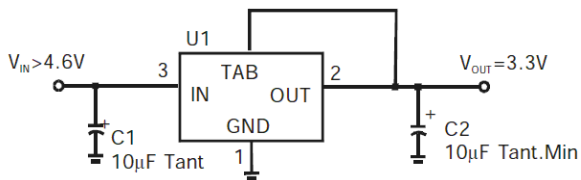
6. V_{OUT} adjustable version only

Block Diagram

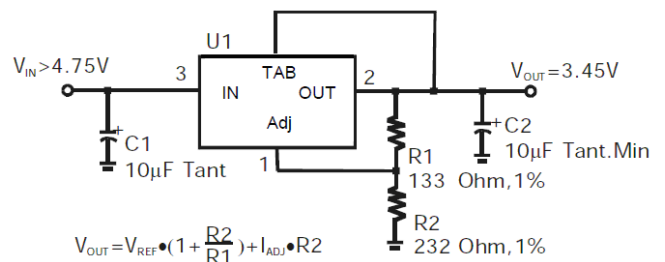


Typical Application Circuit and OTP Function Chart

Fixed Voltage Regulator

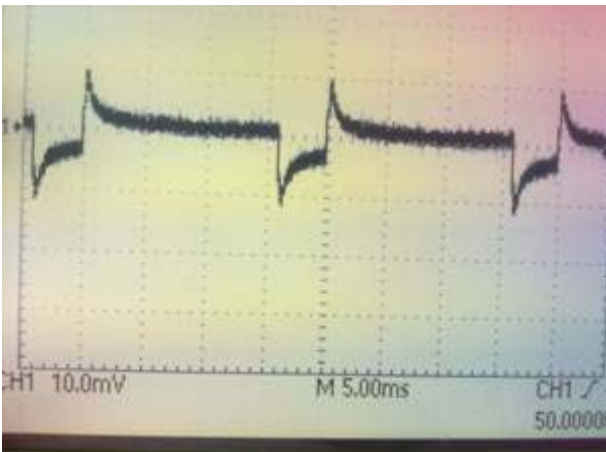


Adjustable Voltage Regulator

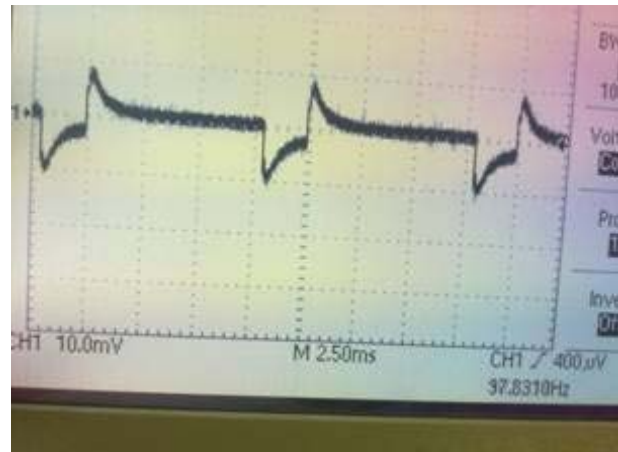


Notes:

- 1) C1 needed if device is far from filter capacitors
- 2) C2 minimum value required for stability



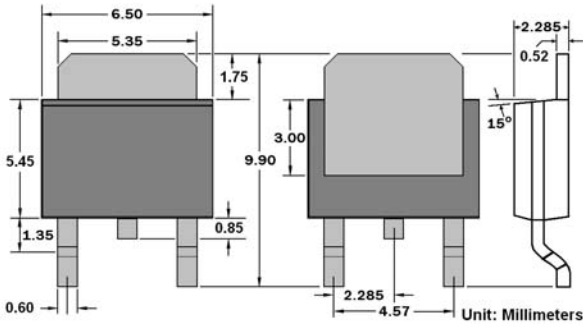
$C_{OUT} = 10\mu F$, $V_{IN} = 5V$, $I_{OUT} = 10mA$ to $1A$, $V_{OUT} = 3.3V$



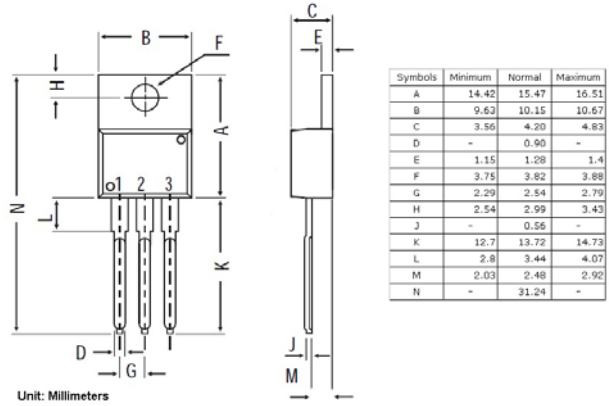
$C_{OUT} = 2.2\mu F$, $V_{IN} = 5V$, $I_{OUT} = 10mA$ to $1A$, $V_{OUT} = 3.3V$

Package Outline

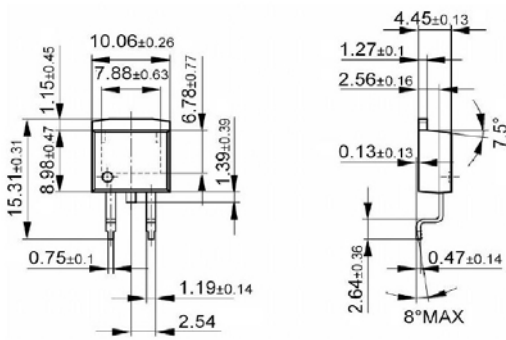
TO-252:



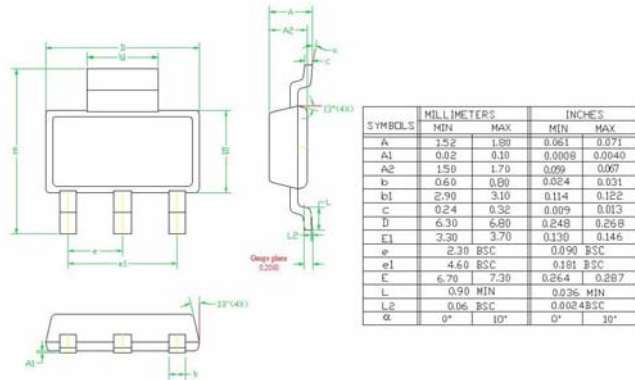
TO-220:



TO-263:



SOT-223:



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